Collimator	IP	Beam	Family	Date of test	Start time	**Status**	Operator comment	Operator	Collimator expert comment						
TCTH.4R1.B2	IP1	B2	TCTH	14-Sep-08	19:24:00	OK		Stefano							
TCTH.4L1.B1	IP1	B1	TCTH	05-Sep-08	6:20:20	OK		Markus							
TCTVA.4L1.B1	IP1	B1	TCTV	05-Sep-08	4:56:54	OK		Markus							
TCTVA.4R1.B2	IP1	B2	TCTV	05-Sep-08	3:50:30	OK		Markus							
TDI.4L2	IP2	B1	INJP												
TCDD.4L2	IP2	B1	INJP	22-Sep-08	17:49:00	OK		Stefano							
TCLIB.6R2.B1	IP2	B1	INJP	14-Sep-08	18:18:00	OK	Energy limit check done on Sep. 16th at 05:23	Stefano							
TCTH.4L2.B1	IP2	B1	TCTH	14-Sep-08	18:49:00	OK	Energy limit check done on Sep. 16th at 05:23	Stefano							
TCTH.4R2.B2	IP2	B2	TCTH	14-Sep-08	18:49:00	OK	Energy limit check done on Sep. 22nd at 19:20	Stefano							
TCP.6L3.B1	IP3	B1	TCP	16-Sep-08	3:09:00	OK		Stefano							
TCP.6R3.B2	IP3	B2	TCP	16-Sep-08	4:41:00	OK		Stefano							
TCSG.B5L3.B2	IP3	B2	TCSG	17-Sep-08	5:22:00	OK		Stefano							
TCSG.B5R3.B1	IP3	B1	TCSG	17-Sep-08	5:22:00	OK		Stefano							
TCSG.A5R3.B1	IP3	B1	TCSG	17-Sep-08	4:50:00	OK		Stefano							
TCSG.A5L3.B2	IP3	B2	TCSG	17-Sep-08	4:50:00	OK		Stefano							
TCSG.5R3.B2	IP3	B2	TCSG	17-Sep-08	2:51:00	OK		Stefano							
TCSG.5L3.B1	IP3	B1	TCSG	17-Sep-08	5:56:00	OK		Stefano							
TCSG.4R3.B1	IP3	B1	TCSG	17-Sep-08	2:20:00	OK		Stefano							
TCSG.4L3.B2	IP3	B2	TCSG	17-Sep-08	2:20:00	OK		Stefano							
TCLA.A5L3.B2	IP3	B2	TCLA	16-Sep-08	5:47:00	OK		Stefano							
TCLA.A5R3.B1	IP3	B1	TCLA	01-Aug-08	19:56:00	**No yet EN**	Preparation for sector test; Now motors are OFF	Stefano							
TCLA.B5L3.B2	IP3	B2	TCLA	17-Sep-08	1:50:00	OK		Stefano							
TCLA.B5R3.B1	IP3	B1	TCLA	01-Aug-08	18:18:00	**No yet EN**	Preparation for sector test; Now motors are OFF	Stefano							
TCLA.6R3.B1	IP3	B1	TCLA	16-Sep-08	5:47:00	OK		Stefano							
TCLA.6L3.B2	IP3	B2	TCLA	04-Sep-08	6:41:13	OK		Markus							
TCLA.7R3.B1	IP3	B1	TCLA				Was blocked due to the problem with the resolver (lost steps).	Stefano							
TCLA.7L3.B2	IP3	B2	TCLA	04-Sep-08	5:23:32	OK		Markus							
TCTH.4L5.B1	IP5	B1	TCTH	13-Sep-08	21:40:00	OK		Stefano							
TCTH.4R5.B2	IP5	B2	TCTH	13-Sep-08	21:51:00	OK		Stefano							
TCTVA.4L5.B1	IP5	B1	TCTV	13-Sep-08	21:58:00	OK		Stefano							
TCTVA.4R5.B2	IP5	B1	TCTV	13-Sep-08	22:03:00	OK		Stefano							
TCDQA.A4R6.B1	IP6	B1	TCDQ	03-Sep-08	10:08:00	OK	Preparation for sector test (done manually with Christophe)	Stefano	Change tol. And keep position fixed: software protection prevents sending settings beyond threshold!						
TCDQA.A4L6.B2	IP6	B2	TCDQ	03-Sep-08	10:30:00	OK	Preparation for sector test (done manually with Christophe)	Stefano	Change tol. And keep position fixed: software protection prevents sending settings beyond threshold!						
TCSG.4R6.B1	IP6	B1	TCDQ	02-Sep-08	19:48:00	OK	Preparation for sector test	Stefano							
TCSG.4L6.B2	IP6	B2	TCDQ	02-Sep-08	18:50:00	OK	Preparation for sector test	Stefano							
TCP.B6L7.B1	IP7	B1	TCP	16-Sep-08	3:09:00	OK	Energy limit check done on Sep. 16th at 05:19	Stefano							
TCP.B6R7.B2	IP7	B2	TCP	16-Sep-08	3:09:00	OK		Stefano							
TCP.C6L7.B1	IP7	B1	TCP	16-Sep-08	4:41:00	OK	Energy limit check done on Sep. 16th at 05:10	Stefano							
TCP.C6R7.B2	IP7	B2	TCP	16-Sep-08	5:47:00	OK		Stefano							
TCP.D6L7.B1	IP7	B1	TCP	16-Sep-08	5:47:00	OK		Stefano							
TCP.D6R7.B2	IP7	B2	TCP	16-Sep-08	5:47:00	OK		Stefano							
TCSG.6R7.B1	IP7	B1	TCSG	17-Sep-08	1:49:00	OK		Stefano							
TCSG.6L7.B2	IP7	B2	TCSG	22-Sep-08	18:51:00	OK		Stefano							
TCSG.A4R7.B1	IP7	B1	TCSG	17-Sep-08	4:50:00	OK		Stefano							
TCSG.A4L7.B2	IP7	B2	TCSG	17-Sep-08	4:50:00	OK		Stefano							
TCSG.A4R7.B2	IP7	B2	TCSG	17-Sep-08	2:51:00	OK		Stefano							
TCSG.A4L7.B1	IP7	B1	TCSG	17-Sep-08	2:51:00	OK		Stefano							
TCSG.A6L7.B1	IP7	B1	TCSG	17-Sep-08	5:22:00	OK		Stefano							
TCSG.A6R7.B2	IP7	B2	TCSG	17-Sep-08	5:22:00	OK		Stefano							
TCSG.A5L7.B1	IP7	B1	TCSG	17-Sep-08	2:20:00	OK		Stefano							
TCSG.A5R7.B2	IP7	B2	TCSG	17-Sep-08	2:20:00	OK		Stefano							
TCSG.B4L7.B1	IP7	B1	TCSG	17-Sep-08	5:56:00	OK		Stefano							
TCSG.B4R7.B2	IP7	B2	TCSG	17-Sep-08	5:56:00	OK		Stefano							
TCLA.A6R7.B1	IP7	B1	TCLA	14-Sep-08	21:15:00	OK	Energy limit check done on Sep. 16th at 05:21	Stefano							
TCLA.A6L7.B2	IP7	B2	TCLA	19-Aug-08	18:19:00	OK	Preparation for sector test; Energy limit check done on Sep. 22nd at 1	Stefano							
TCLA.A7R7.B1	IP7	B1	TCLA	16-Sep-08	1:55:00	OK		Stefano							
TCLA.A7L7.B2	IP7	B2	TCLA	14-Sep-08	21:30:00	OK		Stefano							
TCLA.B6R7.B1	IP7	B1	TCLA	16-Sep-08	2:39:00	OK		Stefano							
TCLA.B6L7.B2	IP7	B2	TCLA	19-Aug-08	19:59:00	OK	Preparation for sector test; Energy limit check done on Sep. 22nd at 1	Stefano							
TCLA.C6R7.B1	IP7	B1	TCLA	14-Sep-08	22:00:00	OK	Energy limit check done on Sep. 16th at 05:20	Stefano							
TCLA.C6L7.B2	IP7	B2	TCLA	14-Sep-08	21:42:00	OK		Stefano							

TCLA.D6R7.B1	IP7	B1	TCLA	14-Sep-08	22:20:00	OK		Stefano								
TCLA.D6L7.B2	IP7	B2	TCLA	16-Sep-08	2:39:00	OK	Energy limit check done on Sep. 16th at 05:19	Stefano								
TDI.4R8	IP8	B2	INJP													
TCLIB.6L8.B2	IP8	B2	INJP	14-Sep-08	18:18:00	OK		Stefano								
TCTH.4L8.B1	IP8	B1	TCTH	13-Sep-08	22:25:00	OK	Energy limit check done on Sep. 22nd at 19:20	Stefano								
TCTH.4R8.B2	IP8	B2	TCTH	13-Sep-08	22:26:00	**lot complet**	Sequence done successfully, but coll is NOT connected to injection B	Stefano								
TCDIH.20607	TI2	B1	TCDIH	22-Sep-08	18:09:59	**Not OK**	Sequence done successfully, but no BIC buffer available.	Stefano								
TCDIH.29465	TI2	B1	TCDIH	17-Sep-08	4:56:00	OK	Coll Environ. Input of BIC was changing!!	Fabio								
TCDIH.29205	TI2	B1	TCDIH	17-Sep-08	4:34:00	OK		Fabio								
TCDIH.29050	TI2	B1	TCDIH	17-Sep-08	3:44:00	OK		Fabio								
TCDIV.29509	TI2	B1	TCDIV	17-Sep-08	6:04:00	OK	di	Fabio								
TCDIV.29234	TI2	B1	TCDIV	17-Sep-08	5:46:00	OK		Fabio								
TCDIV.29012	TI2	B1	TCDIV	17-Sep-08	5:20:00	OK	Occasionally envirnmental parameters removed the beam permit	Fabio								
TCDIH.87441	TI8	B2	TCDIH	17-Sep-08	3:44:00	OK		Fabio								
TCDIH.88121	TI8	B2	TCDIH	17-Sep-08	4:56:00	OK		Fabio								
TCDIH.87904	TI8	B2	TCDIH	17-Sep-08	4:34:00	**Gap not OK**	Problem with gap checks: one gap badly calibrated, always outside w	Fabio	Need to repeat gap sequence after re-calibration!							
TCDIV.88123	TI8	B2	TCDIV	17-Sep-08	6:04:00	OK		Fabio								
TCDIV.87645	TI8	B2	TCDIV	17-Sep-08	5:20:00	OK		Fabio								
TCDIV.87804	TI8	B2	TCDIV	17-Sep-08	5:46:00	OK		Fabio								